About IEEE IEEE Mem	berships Products and Services Conferences IEEE Organizations News Home
�IEEE	Xplore TM Help FAQ Terms Technical Update
Welcome to IEEE Xplores	
O- Home	Your search matched 2 of 649168 documents. Results are shown 25 to a page, sorted by publication year in descending order.
O- Log-out	You may refine your search by editing the current search expression or entering a new one the t
	Then click Search Again .
Tables of Contents	burst <near 5=""> register</near>
O- Journals & Magazines	Search Again
Conference Proceedings	Results: Journal or Magazine = JNL Conference = CNF Standard = STD
O- Standards	
	1 Decoding of parallel Reed-Solomon codes with applications to pro
Search	concatenated codes
O- By Author	Krachkovsky, V.Yu.; Yuan Xing Lee
O- Basic	Information Theory, 1998. Proceedings. 1998 IEEE International Symposium
O- Advanced	
	Page(s): 55
Member Services	[Abstract] [PDF Full-Text] CNF
O- Join IEEE	
C Establish IEEE Web Account	2 A 200 MHz register-based wave-pipelined 64 M synchronous DRAN Ho-Jun Song; Jung-Pill Kim; Jae-Jin Lee; Jong-Hoon Oh; Seung-Han Ahn; I
	Hwang
	Solid-State Circuits, IEEE Journal of , Volume: 32 Issue: 1 , Jan. 1997
	Page(s): 92 -99
	[Abstract] [PDF Full-Text] JNL

Home | Log-out | Journals | Conference Proceedings | Standards

Search by Author | Basic Search | Advanced Search | Join IEEE | Establish a Web Account

Copyright © 2000 IEEE -- All rights reserved

	About IEEE IEEE Men	nberships Products and Services Conferences IEEE Organizations News Home
:	∲IEEE	Xplore™ Help FAQ Terms Technical Update
	Welcome to IEEE <i>Xplore</i> ** O- Home O- Log-out	Your search matched 1 of 649734 documents. Results are shown 25 to a page, sorted by publication year in descending order. You may refine your search by editing the current search expression or entering a new one the tellick Search Again.
	Tables of Contents - Journals & Magazines - Conference Proceedings - Standards	echo clock Search Again Results: Journal or Magazine = JNL Conference = CNF Standard = STD
	Search - By Author - Basic - Advanced	1 An 833 MHz 1.5 W 18 Mb CMOS SRAM with 1.67 Gb/s/pin Pilo, H.; Allen, A.; Covino, J.; Hansen, P.; Lamphier, S.; Murphy, C.; Traver, P. Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 20 International, 2000 Page(s): 266-267
	Member Services - Join IEEE - Establish IEEE Web Account	[Abstract] [PDF Full-Text] CNF

Home | Log-out | Journals | Conference Proceedings | Standards

Search by Author | Basic Search | Advanced Search | Join IEEE | Establish a Web Account

Copyright © 2000 IEEE -- All rights reserved